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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

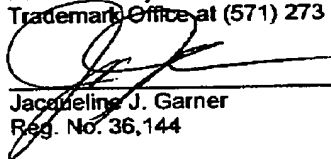
Applicant: Masood Murtuza Art Unit: 2826  
Serial No.: 10/726,474 Examiner: Baumgardner, M.  
Filing Date: 12/01/2003 Docket No.: T1-35639  
Customer No.: 23494 Conf. No.: 5891  
Title: SUPPORT STRUCTURE FOR LOW-K DIELECTRICS

**INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.56**

Commissioner of Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at (571) 273 - 8300 on 2/20/2006.

  
Jacqueline J. Garner  
Reg. No. 36,144

Dear Sir:


An attached PTO/SB/08A lists a U.S. Patent which Applicant believes is relevant to the subject patent application. Applicant submits this reference to aid the Examiner in the examination of this application.

This Information Disclosure Statement is submitted prior to the mailing of any final action under § 1.113, a notice of allowance under § 1.311, or an action that otherwise closes prosecution pursuant to 37 C.F.R. § 1.97(c).

Please charge the fee under 37 CFR 1.17(p) and any other fees in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 20-0668 of Texas Instruments Incorporated.

Texas Instruments Incorporated  
P.O. Box 655474, MS 3999  
Dallas, TX 75265  
(214) 532-9348

Respectfully submitted,

  
Jacqueline J. Garner  
Attorney for Applicants  
Reg. No. 36,144

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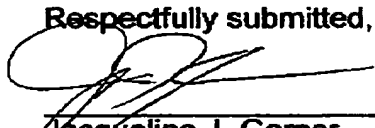
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